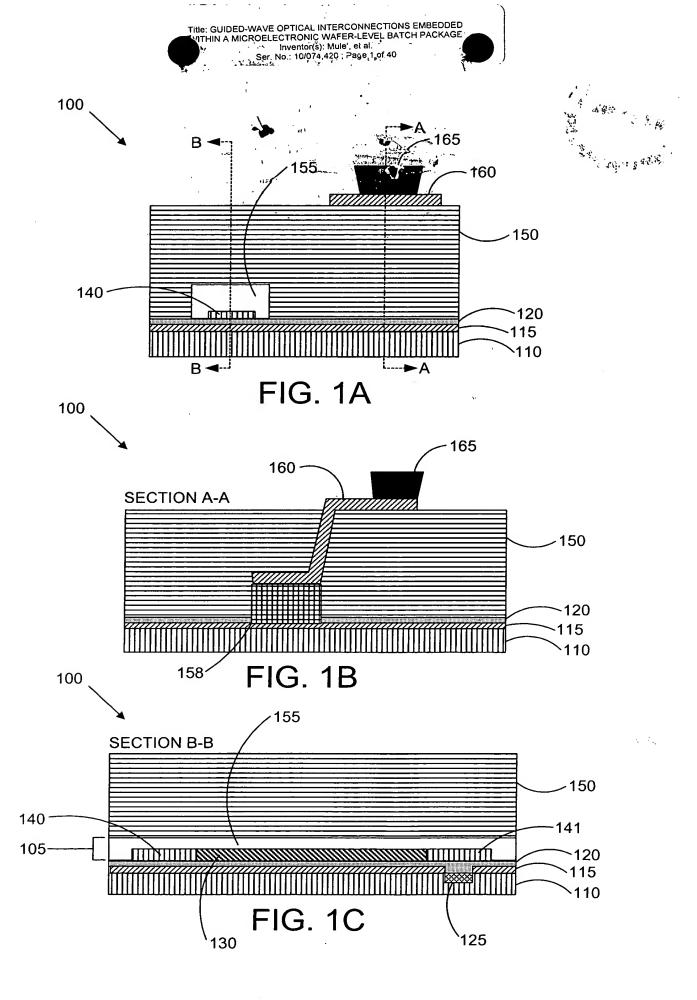
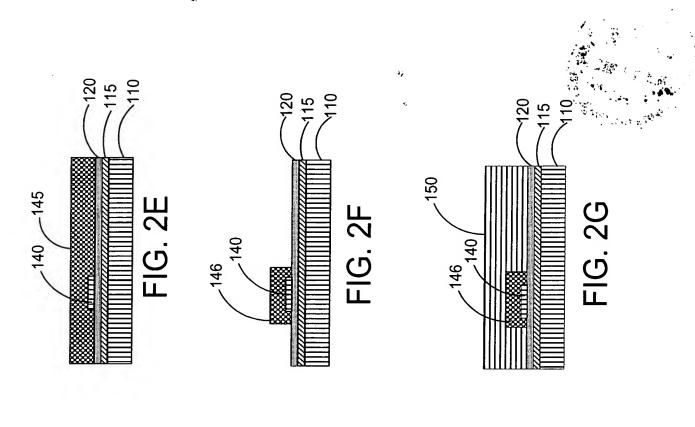
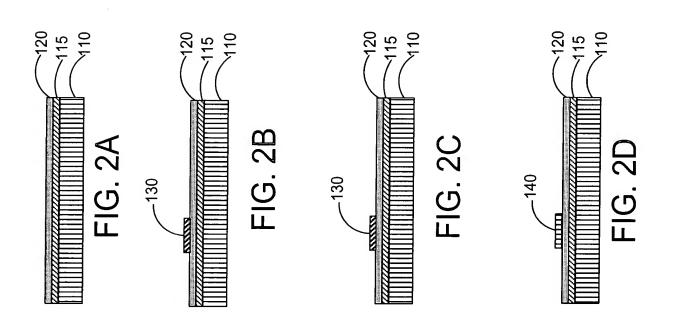
PF



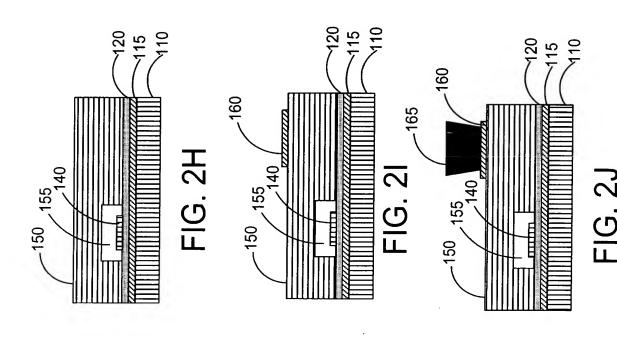


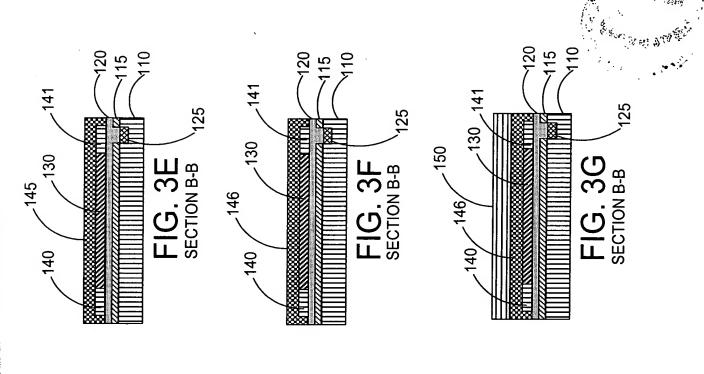


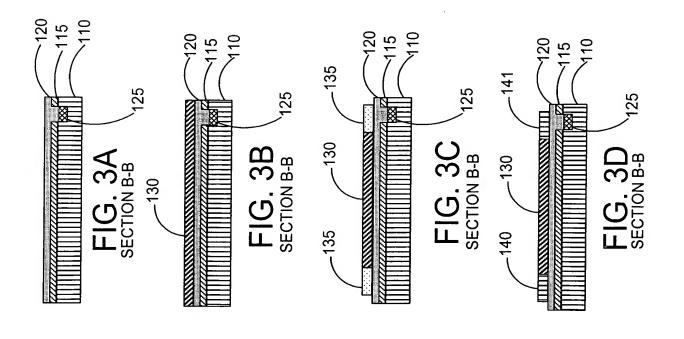
Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED THIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE Inventor(s): Mule', et cl.

Ser. No.: 10/074,420; Page 3 of 40





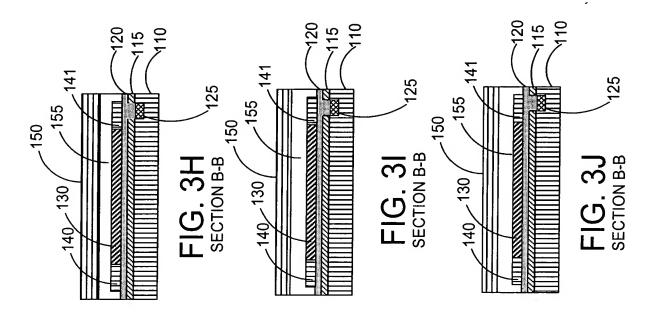


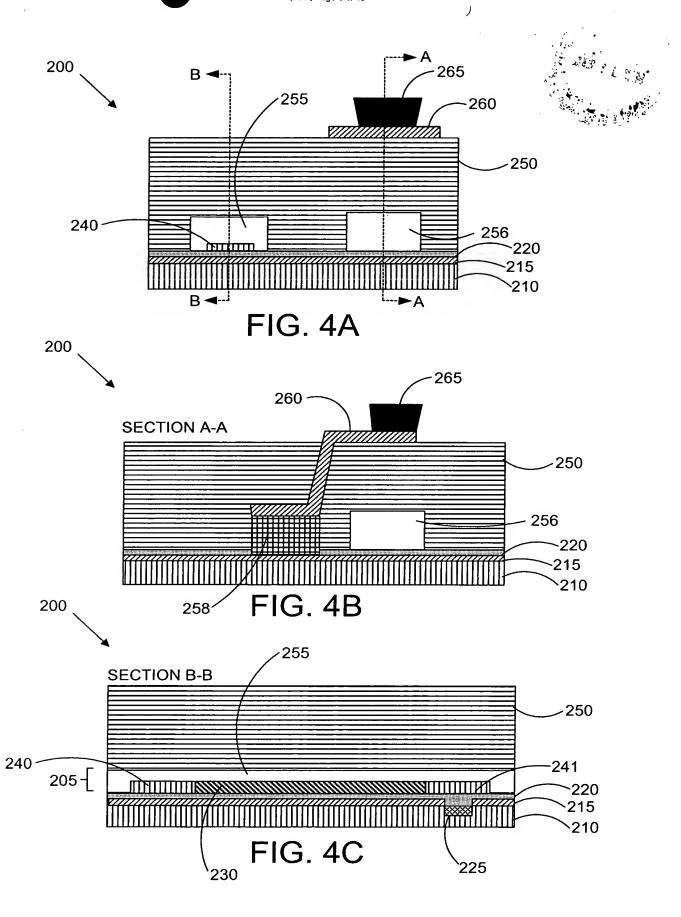


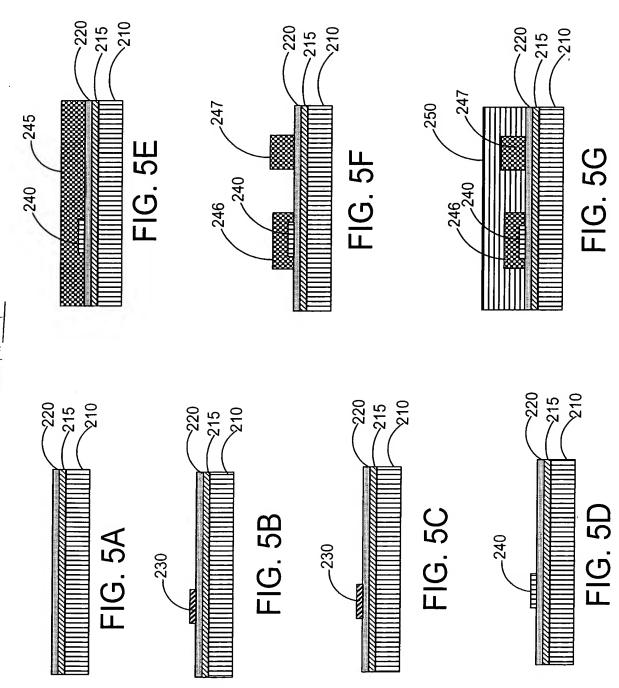
Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED HIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE Inventor(s): Mule', et al.

Ser. No.: 10/074,420; Page 5 of 40









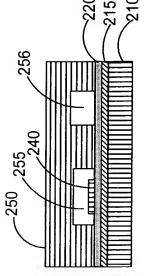


FIG. 5H

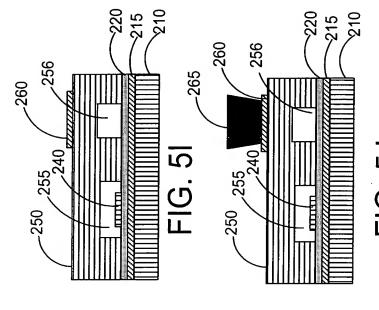
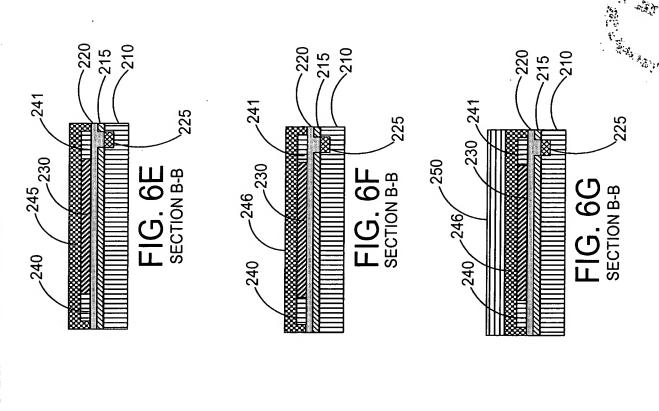
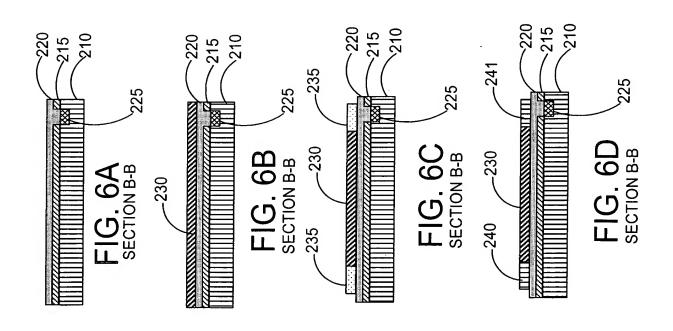
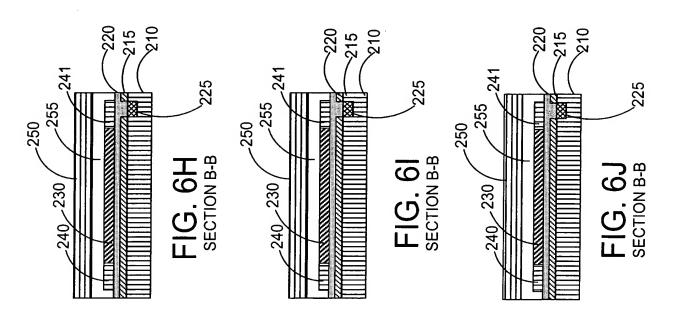


FIG. 5J



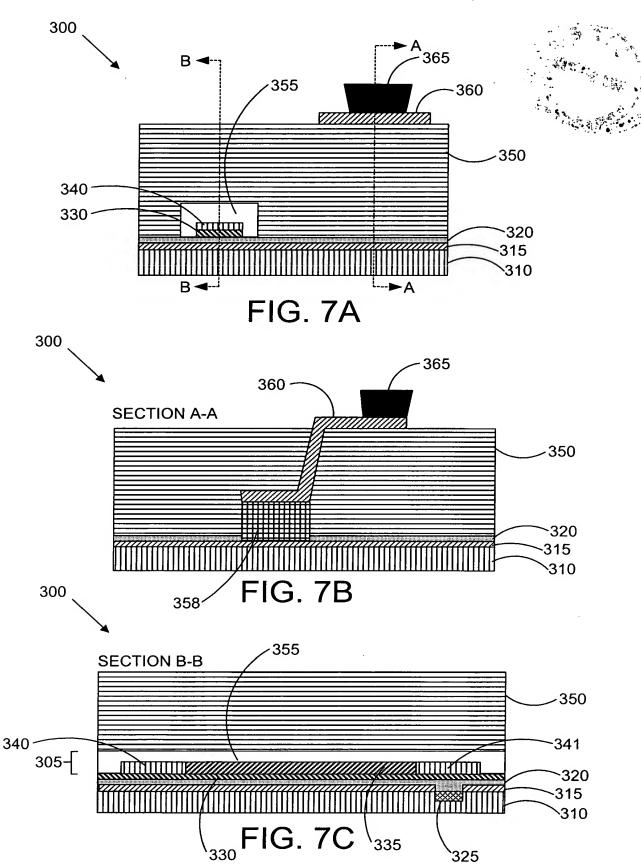


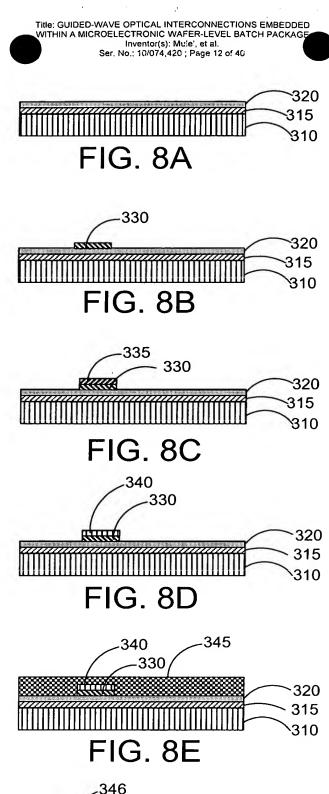


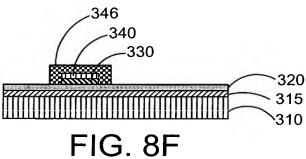


Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED (THIN A MICROELECTRONIC WAFER-LE'VEL BATCH PACKAGE Inventor(s): Mule', et al.

Ser. No.: 10/074,420; Page 11. of 40







Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED
WITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE
Inventor(s): Mule', et al.
Ser. No.: 10/074,420; Page 13 of 4...

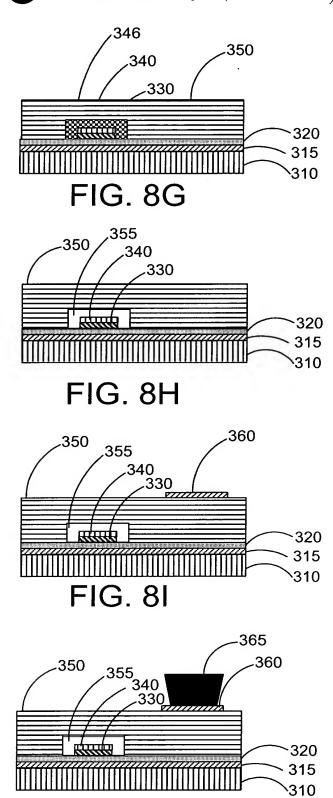
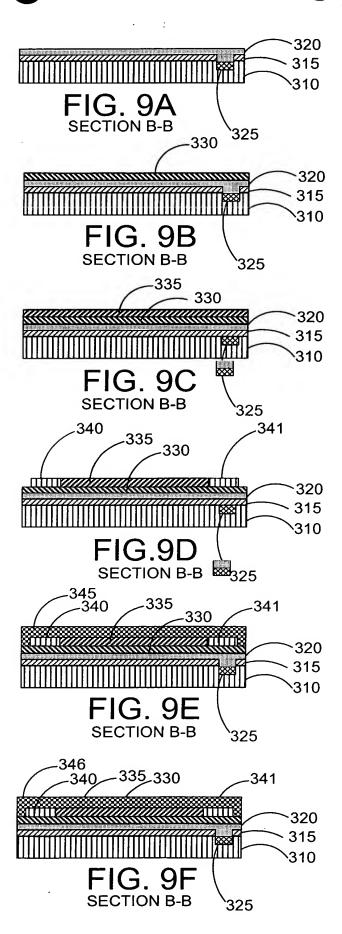


FIG. 8J

Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED WITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE Inventor(s): Mule', et al.

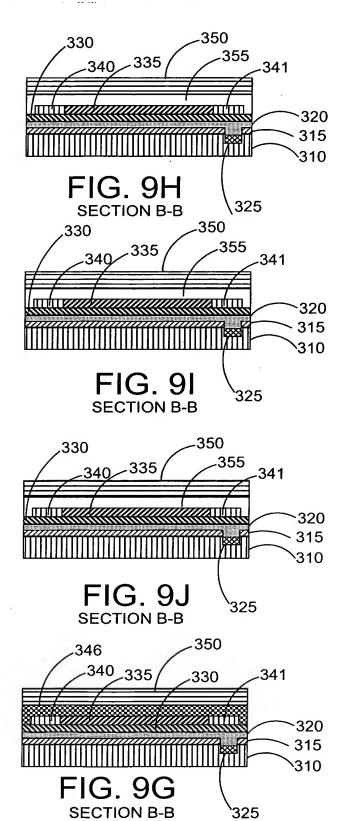
Ser. No.: 10/074,420; Page 14 of 40





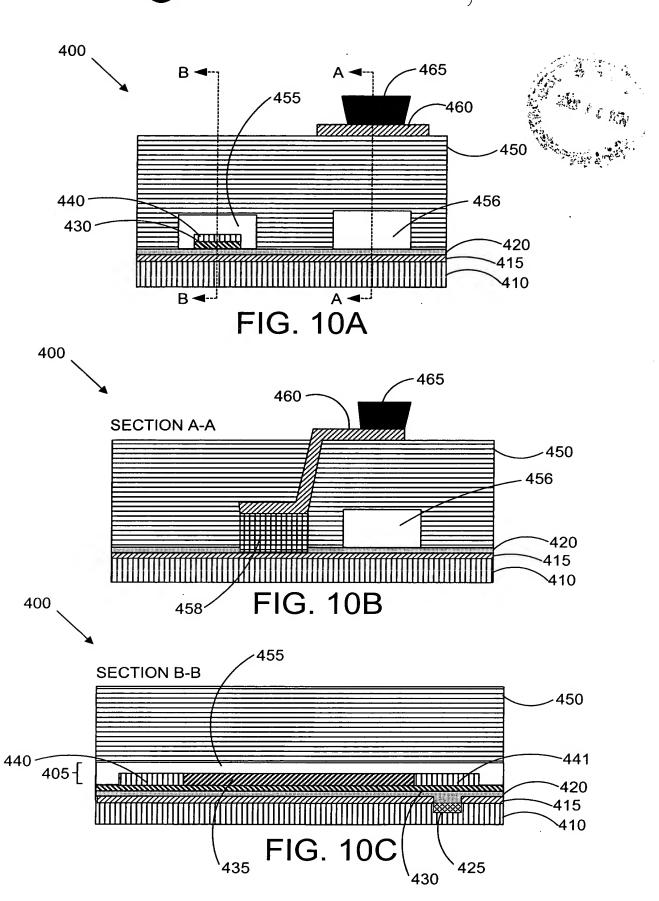
Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED ITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAG Inventor(s): Mule', et al.

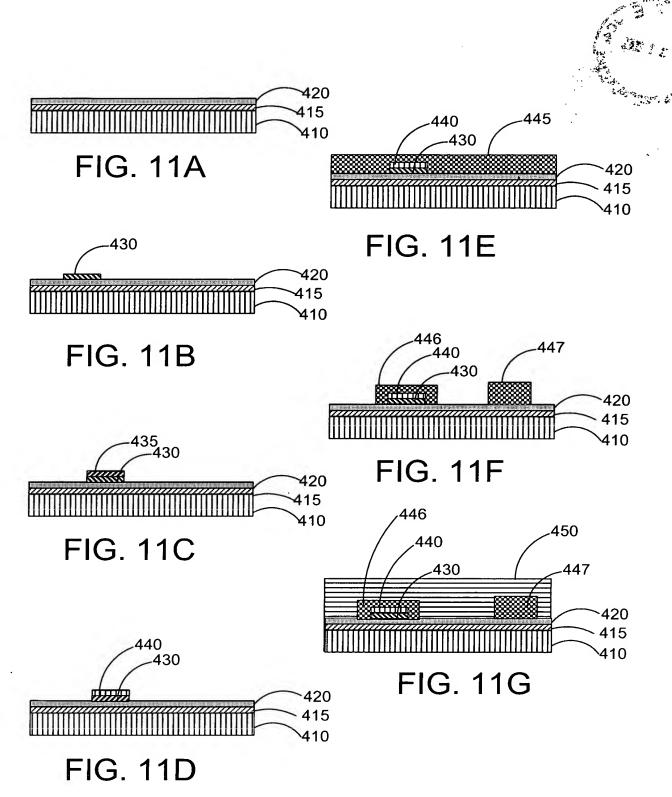
Ser. No.: 10/074,420; Page 15 of 10



Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED WITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE Inventor(s): Mule', et al.

Ser. No.: 10/074,420; Page 16 of 40







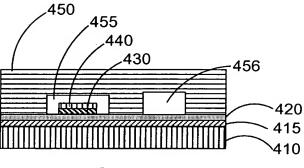
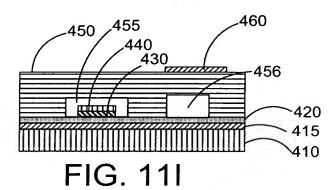


FIG. 11H



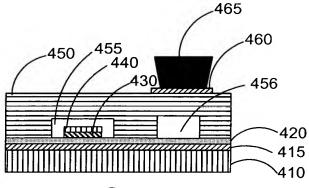
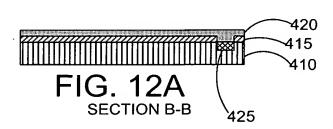
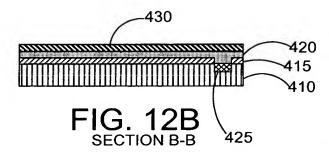
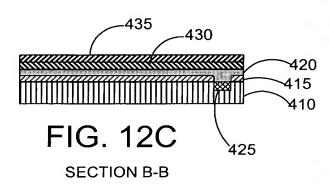
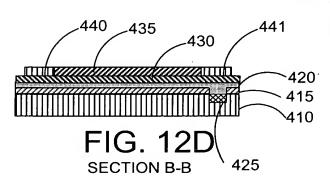


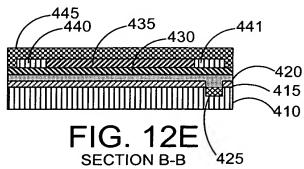
FIG. 11J

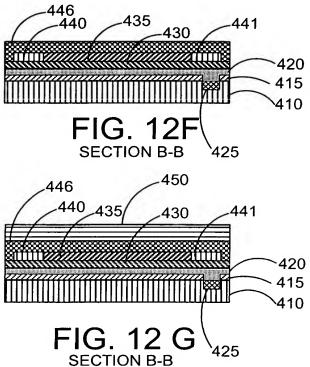






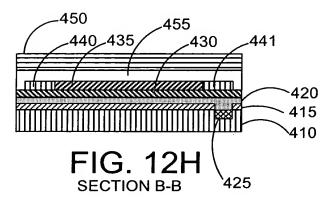


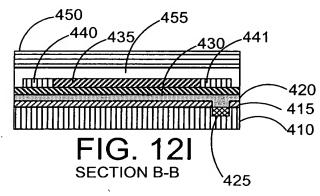


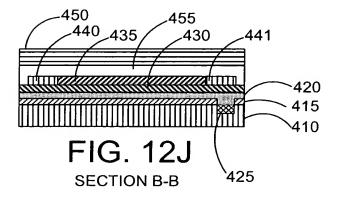




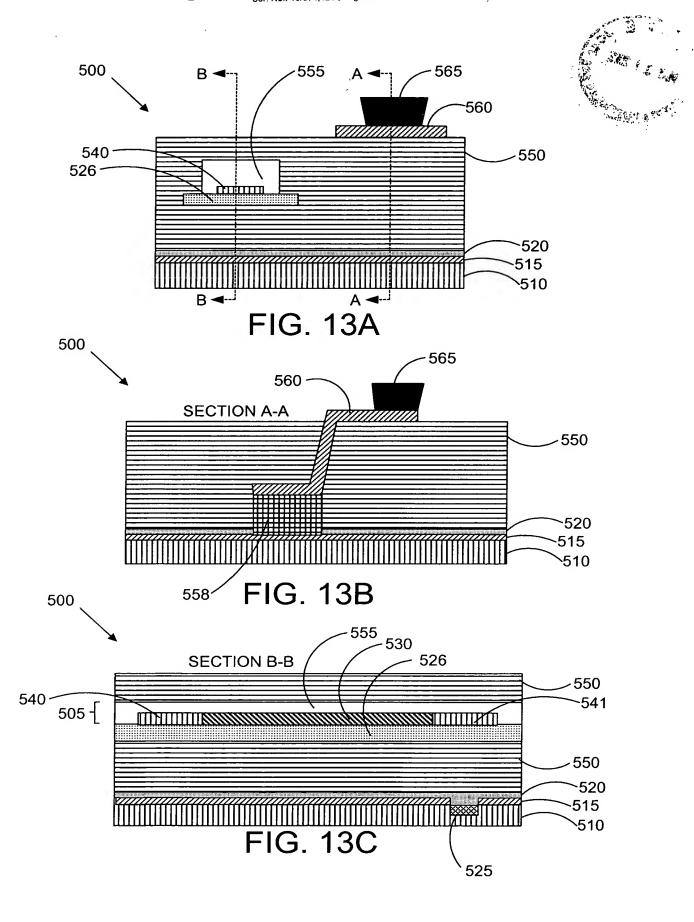




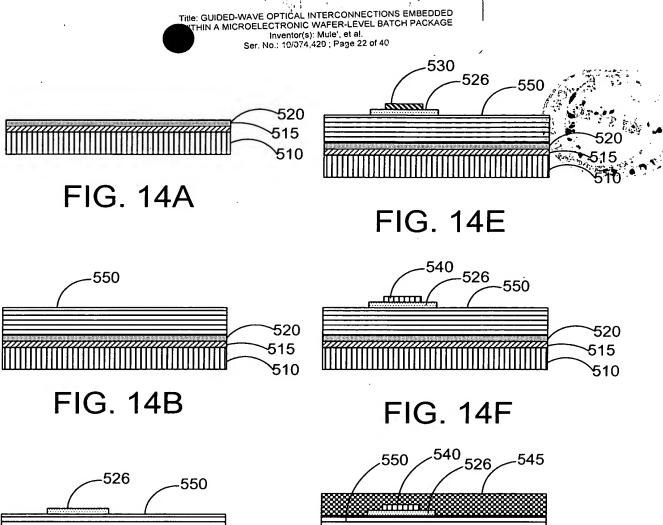




THE: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED
HIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE
Inventor(s): Mule', et al.
Ser. No.: 10/074,420; Page 21 of 45



The state of the s



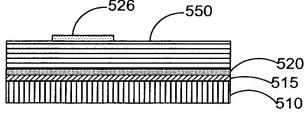


FIG. 14C

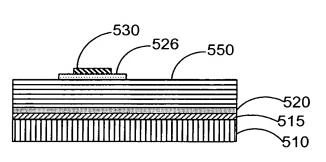


FIG. 14D

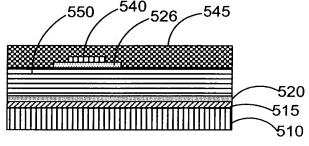
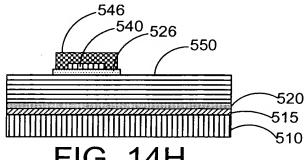
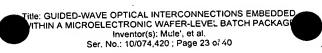


FIG. 14G



**FIG. 14H** 



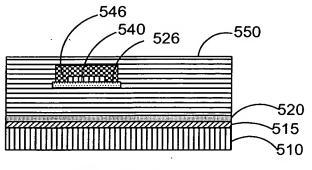


FIG. 141

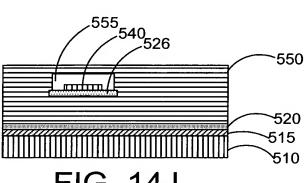
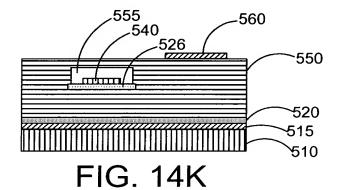


FIG. 14J

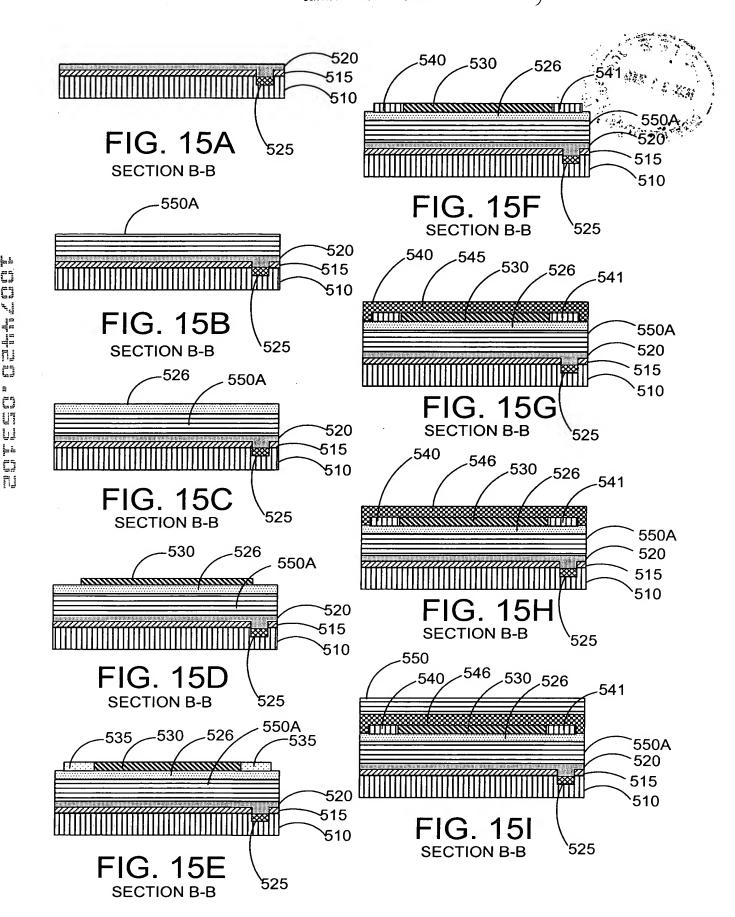


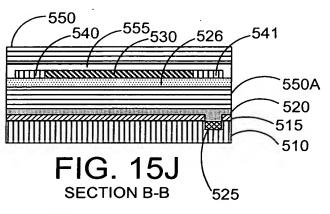
\_540 

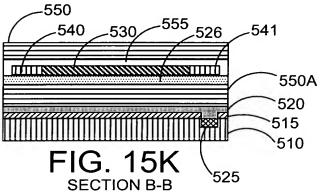
FIG. 14L

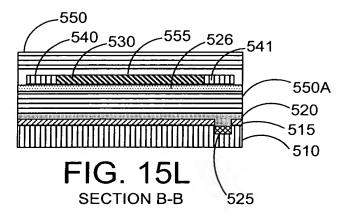
Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED ITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE Inventor(s): Mule', et al.

Ser. No.: 10/074,420; Page 24 of /:C







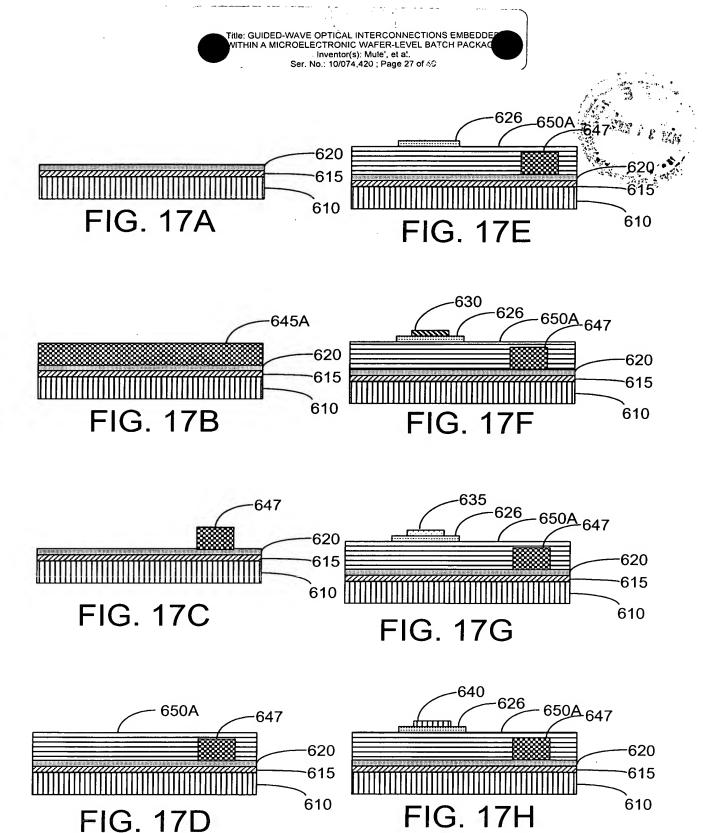


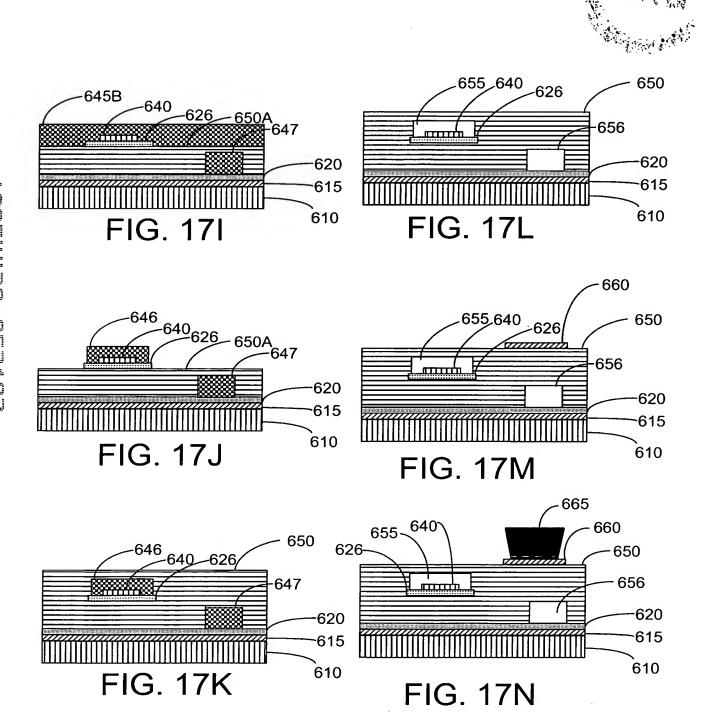
Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED WITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACK Inventor(s): Mule', et al.

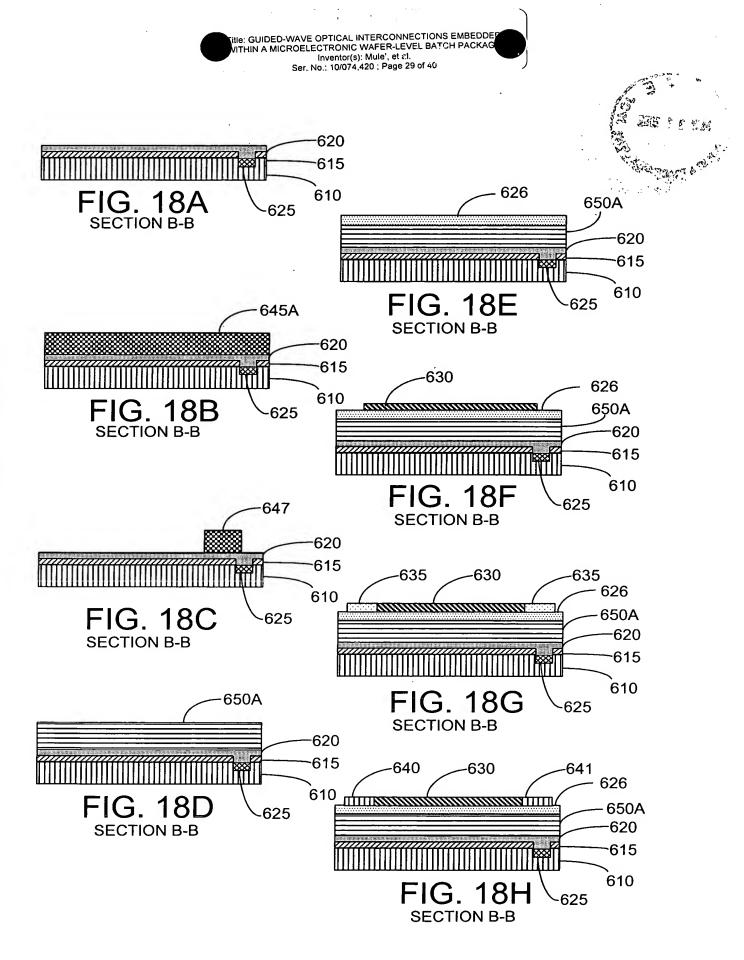
Ser. No.: 10/074,420; Page 23 of 40 A -665 FIG. 16A 660 -**SECTION A-A** ·656 FIG. 16B -655 **SECTION B-B** 60<sup>5</sup> 

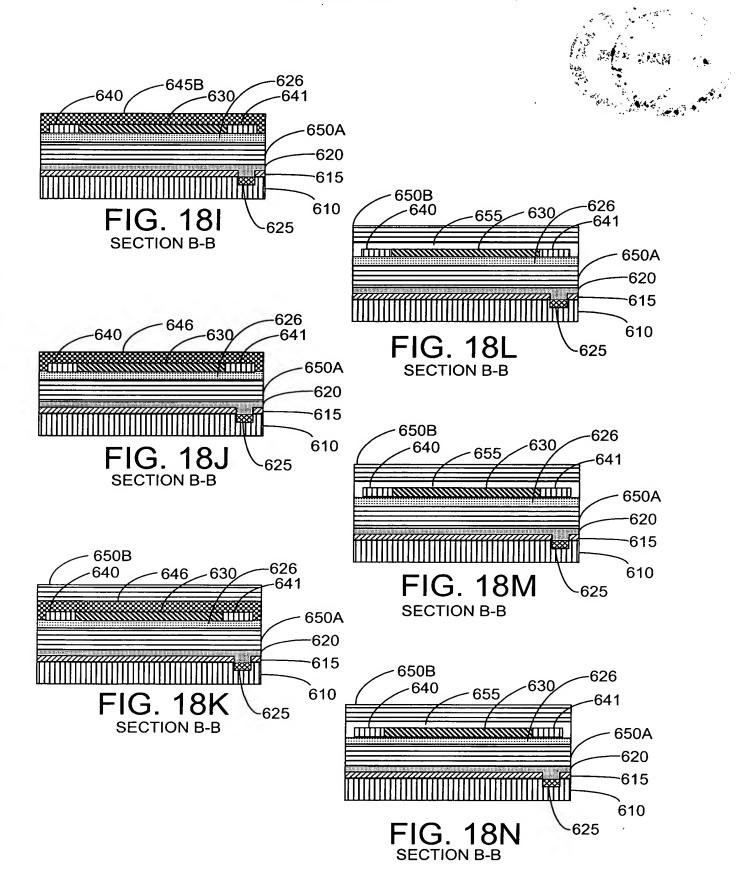
FIG. 16C

**`**610





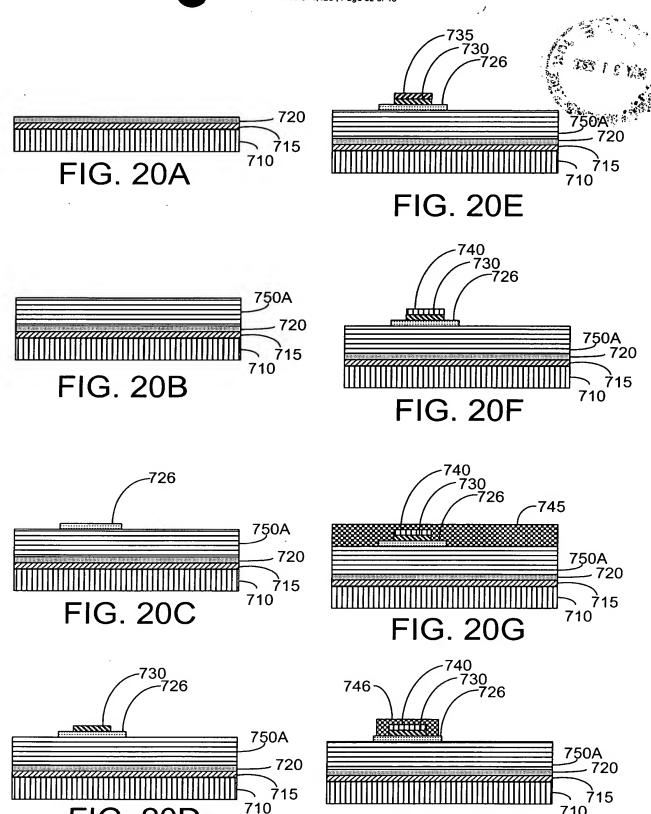




Inventor(s): Mule', et al. Ser. No.: 10/074,420 ; Page 31 of 40 730-FIG. 19A **SECTION A-A** FIG. 19B 758-**SECTION B-B** 740-FIG. 19C 

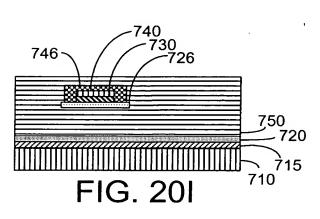
Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED WITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE

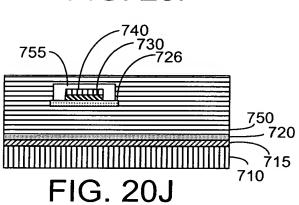
FIG. 20D

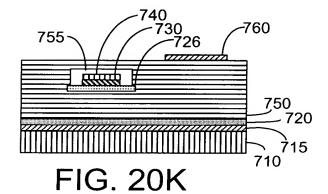


710

FIG. 20H







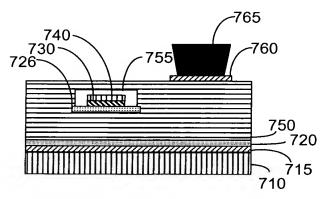
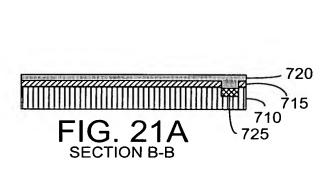
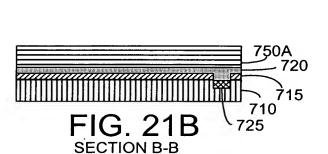
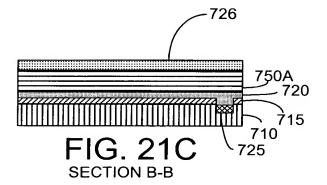
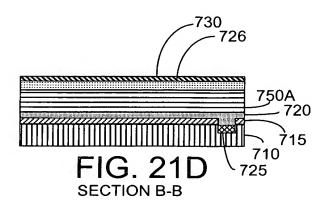


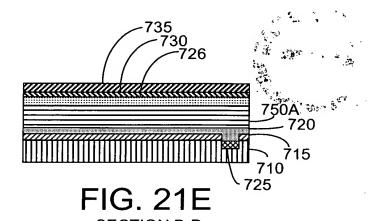
FIG. 20L

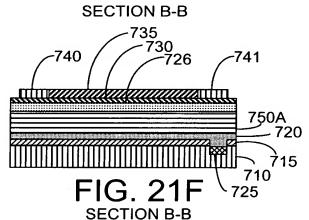


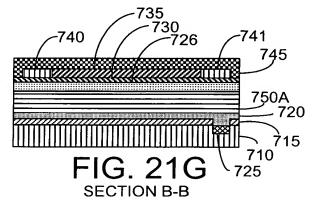


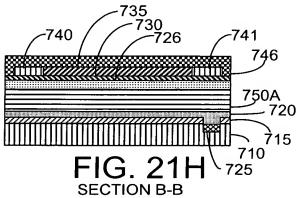


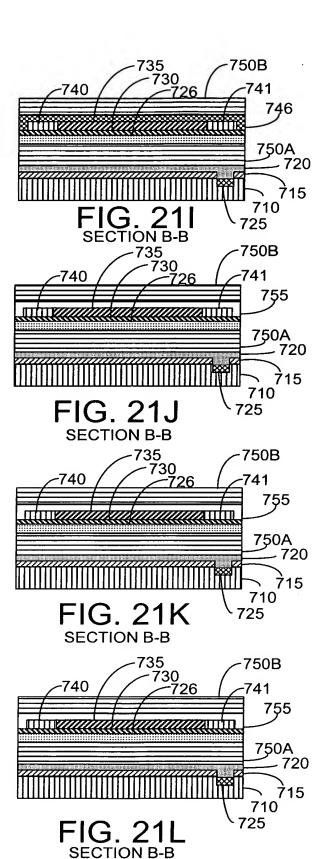








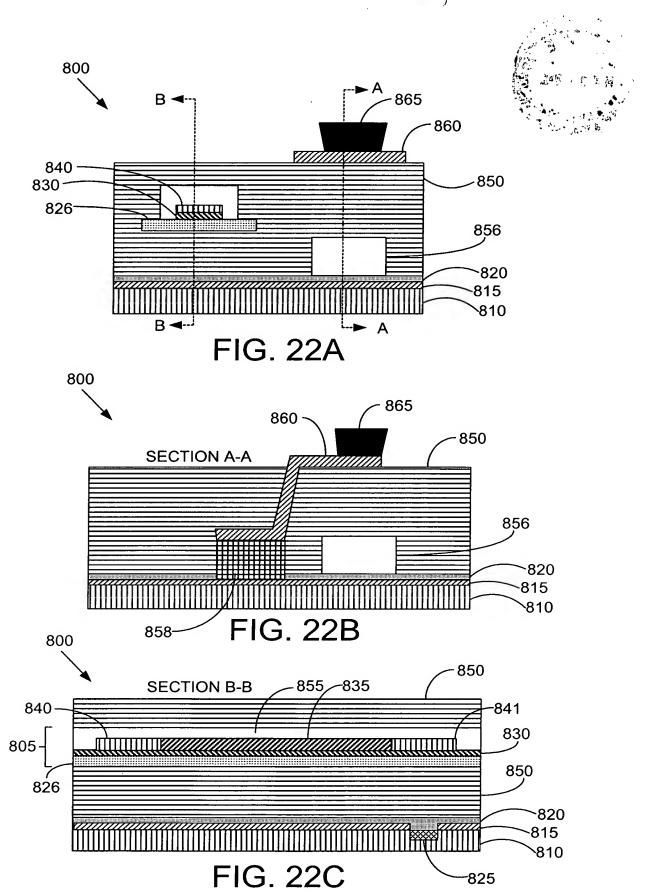






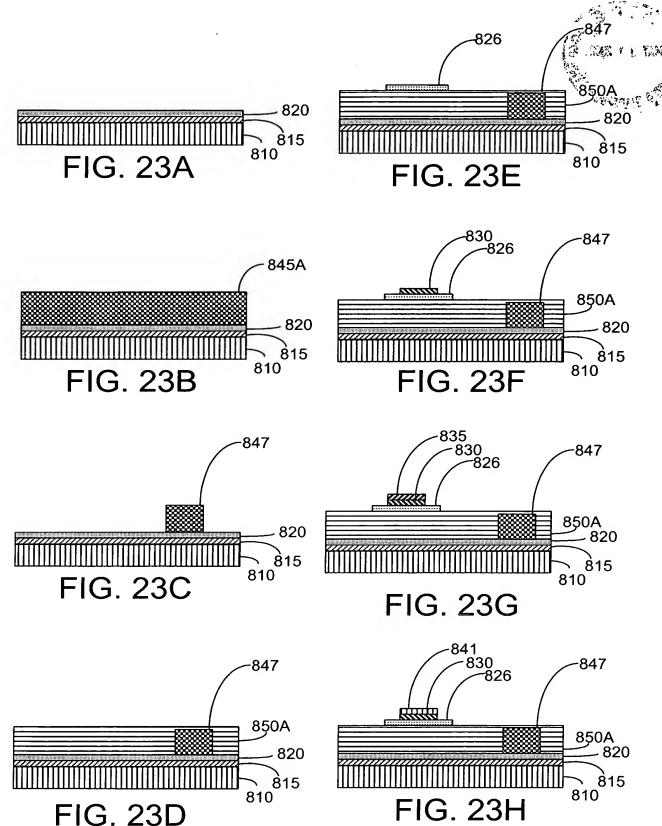
Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED WITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE Inventor(s): Mule\*, et al.

Ser. No.: 10/074,420; Page 33 of 43

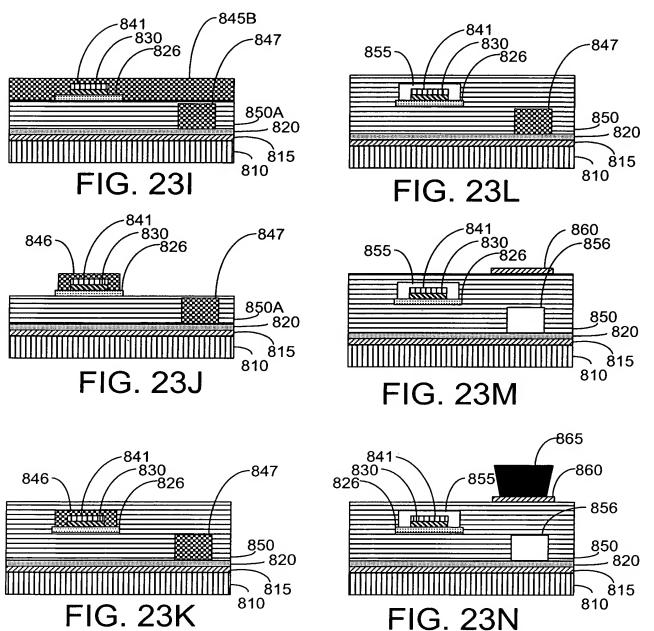


The transmitter of the transmitter.

Line hall he have deed then my







## Title: GUIDED-WAVE OPTICAL INTERCONNECTIONS EMBEDDED WITHIN A MICROELECTRONIC WAFER-LEVEL BATCH PACKAGE Inventor(s): Mule', et al. Ser. No.: 10/074,420 ; Page 39 of 49

